

LIST OF REFERENCES CITED BY APPLICANT Form PTO-1449 <i>(Use several sheets if necessary)</i>				ATTY. DOCKET NO.: 4717-11300	APPLICATION NO.:			
				APPLICANT: Claire RICHTARCH				
				FILING DATE: September 25, 2003	GROUP:			
<b>U.S. PATENT DOCUMENTS</b>								
*EXAMINER INITIAL	*CITE IF APPROPRIATE	TRADEMARK OFFICE	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>WLL</i>	AA		5,895,583	4/1999	Augustine et al.	216	53	
<b>FOREIGN PATENT DOCUMENTS</b>								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
YES	NO							
<i>WLL</i>	AB	0 966 034	12/1999	EPO			X	
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
<i>WLL</i>	AC	Bruel, M. et al., "Smart-Cut": A Promising New SOI Material Technology, IEEE SOI Conference, October 1995, pp 178-179.						
<i>WLL</i>	AD	Maleville et al., "Wafer Bonding and H-Implantation Mechanisms Involved In The Smart-Cut Technology", Materials Science and Engineering, 1997, pp. 14-19.						
<b>EXAMINER</b> <i>Walter J. Tarczynski Jr.</i>				<i>2/9/05</i>				
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								
A M								

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LIST OF REFERENCES CITED BY APPLICANT Form PTO-1449 (Use several sheets if necessary)				ATTY. DOCKET NO.: 4717-11300	APPLICATION NO.: 10/671,812		
				APPLICANT: Claire RICHTARCH			
				FILING DATE: September 25, 2003	GROUP: 1762		
U.S. PATENT DOCUMENTS							
*EXAMINEE R INITIAL	CITE NO.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA							
AB							
AC							
AD							

FOREIGN PATENT DOCUMENTS

AE

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
WL	AE	FR 02-09869	8/2000	France	—	—	X	
	AF							
	AG							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

WL	AH	A. J. Auberton-Hervé et al., "Why Can SMART-CUT® Change the Future of Microelectronics?", International Journal of High Speed Electronics and Systems, Vol. 10, no. 1, 2000, pp 131-146.
WL	AI	"Thermal and Dopant Processes", Chapter 4, Advanced Semiconductor Fabrication Handbook, ICE, 1998
	AJ	
	AK	

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